20200101 T. Bourke

ATmega48A/PA/88A/PA/168A/PA/328/P §22 2-wire Serial Interface: state machine

- bold lines: interrupt (or polling of INT)
- dashed lines: INT = 0
- other lines: INT = 1
- dotted states: waiting for TWI

Setting EN=0 disables the TWI.
EA/EA determines possibility of entry into SR and ST on arbitration loss (not distinguished after "not addressed" states).

0xF8 No state info.